



P6025 Analogue Input PCB.

**Before attempting any repairs, read the notes on page 3..**

VR1 CMRR 'nulling' - This should not be adjusted.

VR2 Amplifier gain. - All modules leave the factory with the same gain and should be within 0.2 dB of each other.

VR3 Bias adjustment - All modules leave the factory fully aligned and this should only be adjusted when repairs to the power module have been made. There are two methods of aligning the bias, as follows:-

1. Current method. Remove the +ve fuse (FS2 or FS3) on the power supply PCB (PCB 605\_2 ), and replace with a DC current meter. - set the meter to the high current range. - Do not use any load, or signal. - switch the amplifier on and adjust the preset so that the module draws 180 milliamps. This should preferably be set when the amplifier has warmed up a little.

2. THD method. After setting the bias to the nominal level, as above, replace the fuse and plug in the resistive load. - Inject a 1 KHz signal into the amplifier, and connect the distortion analyser's input to the speaker connectors at the rear panel. ( note the signal generator output and the analyser input should both be balanced, otherwise false readings may occur ) - Set the band-width of the analyser to 20 - 20KHz. - Switch on the amplifier, set the signal level to about 2 dBs below clipping.....