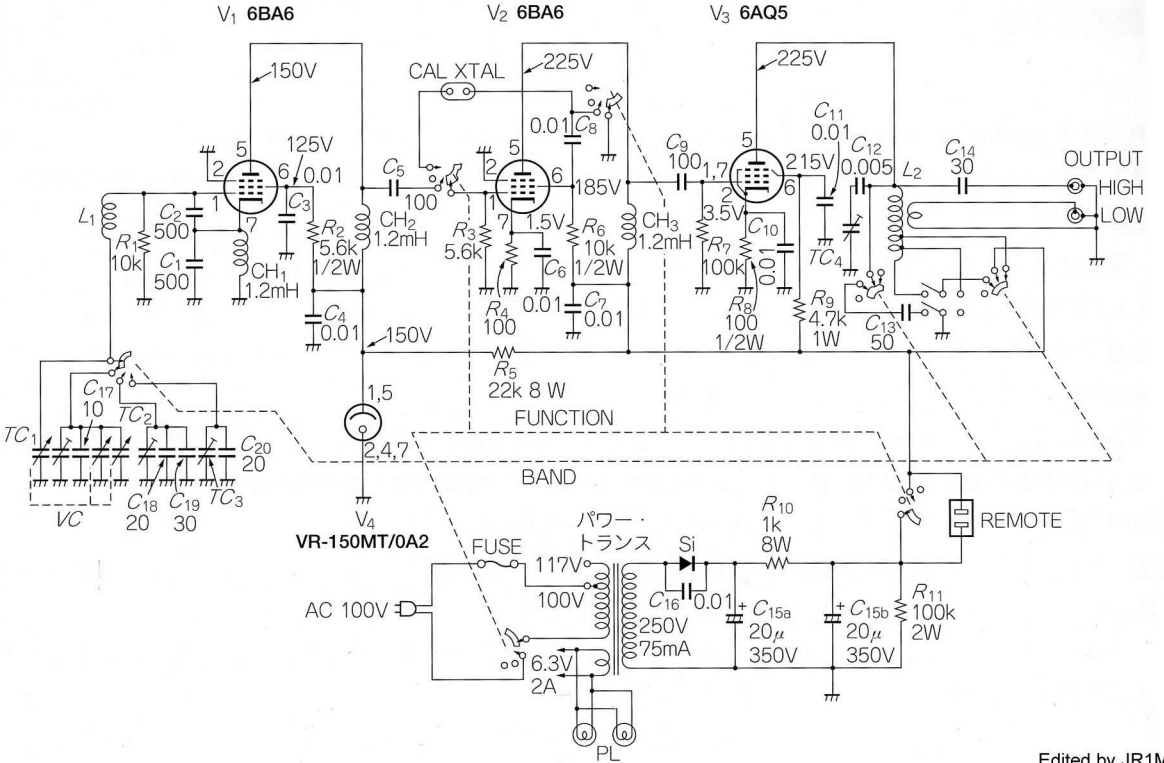
TRIO VFO-1 回路図



Edited by JR1MAF