

EQ 210/110

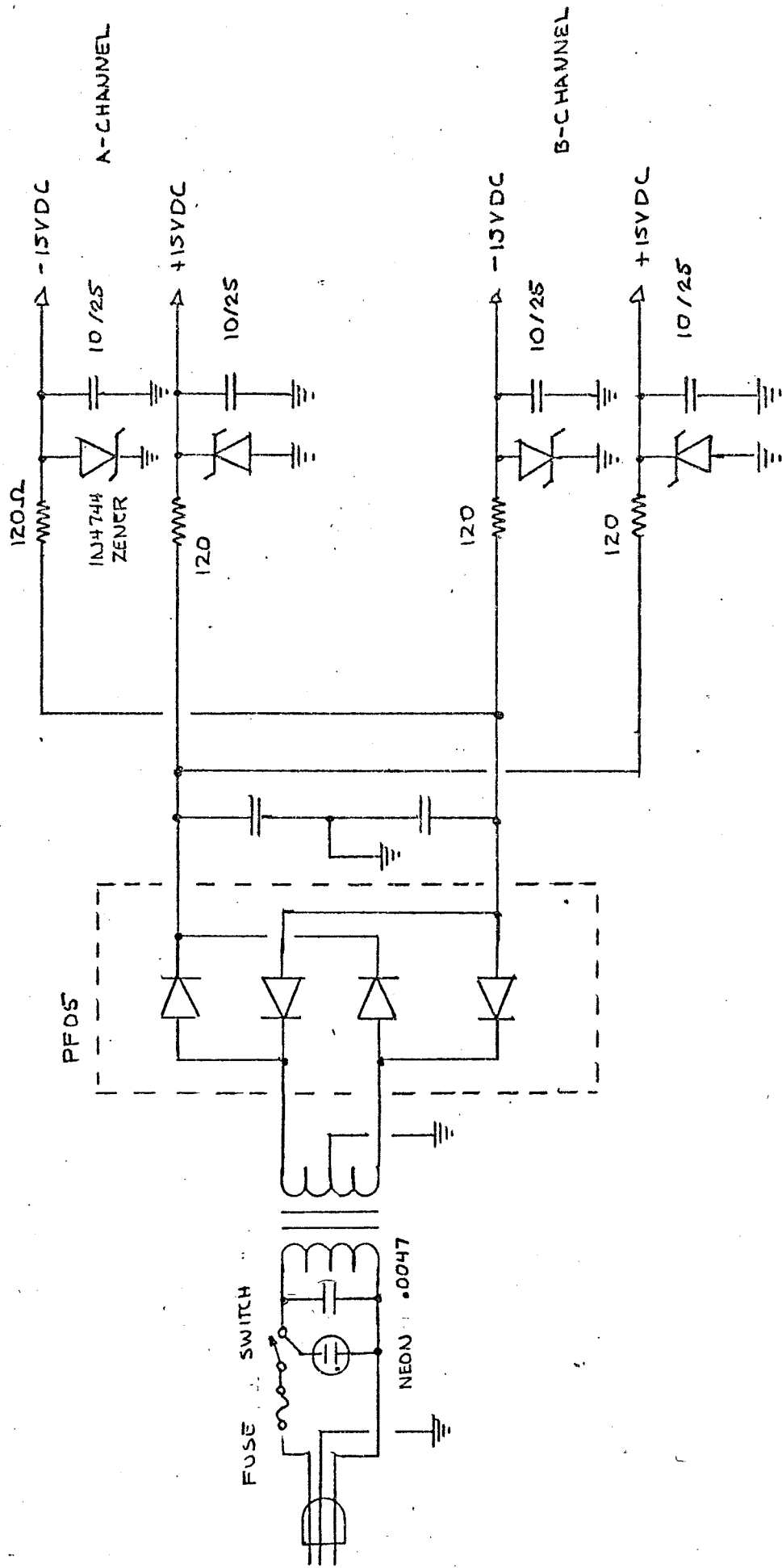
# Schematic

**B I A M P<sup>®</sup>**

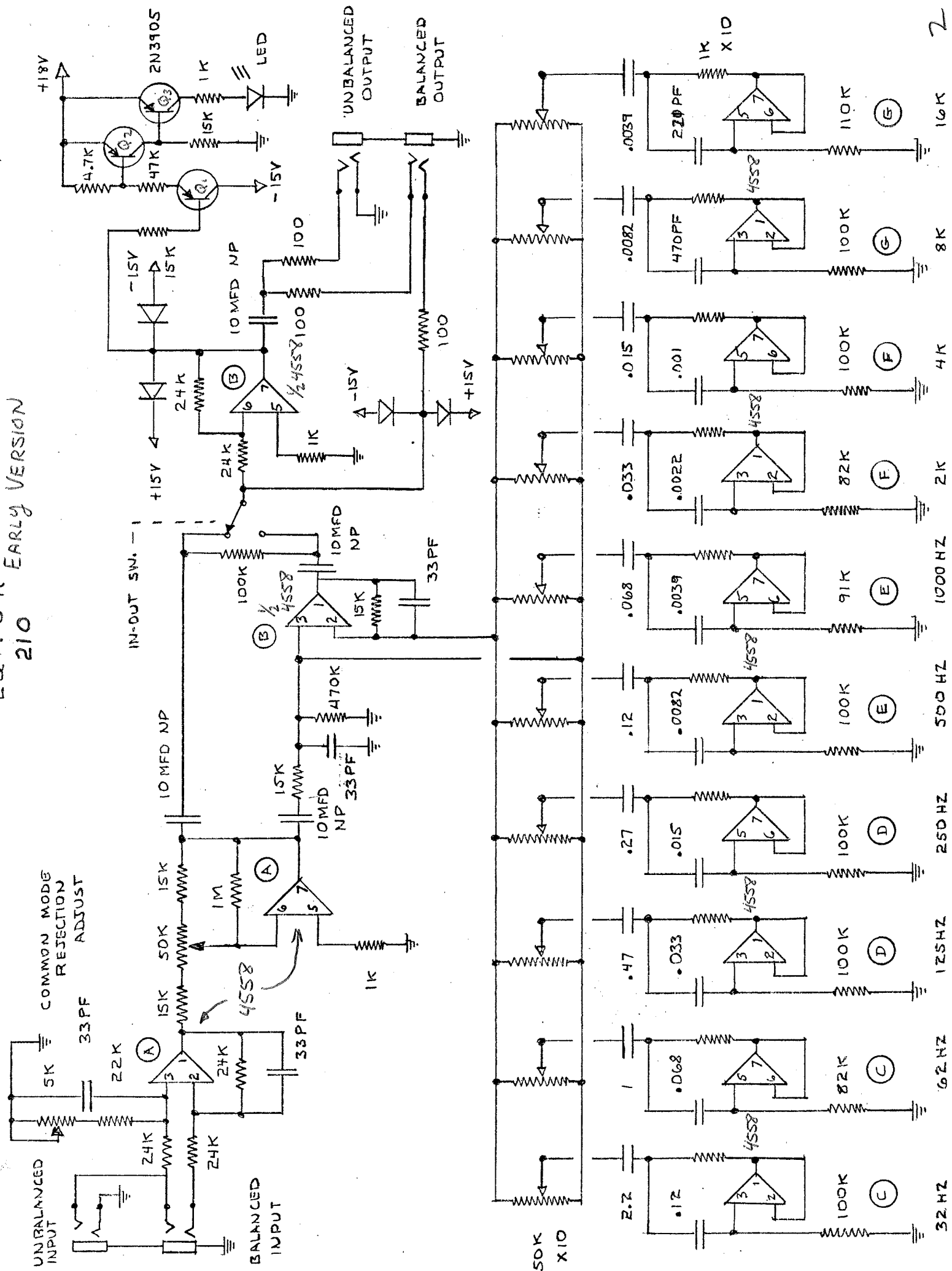
---

S Y S T E M S

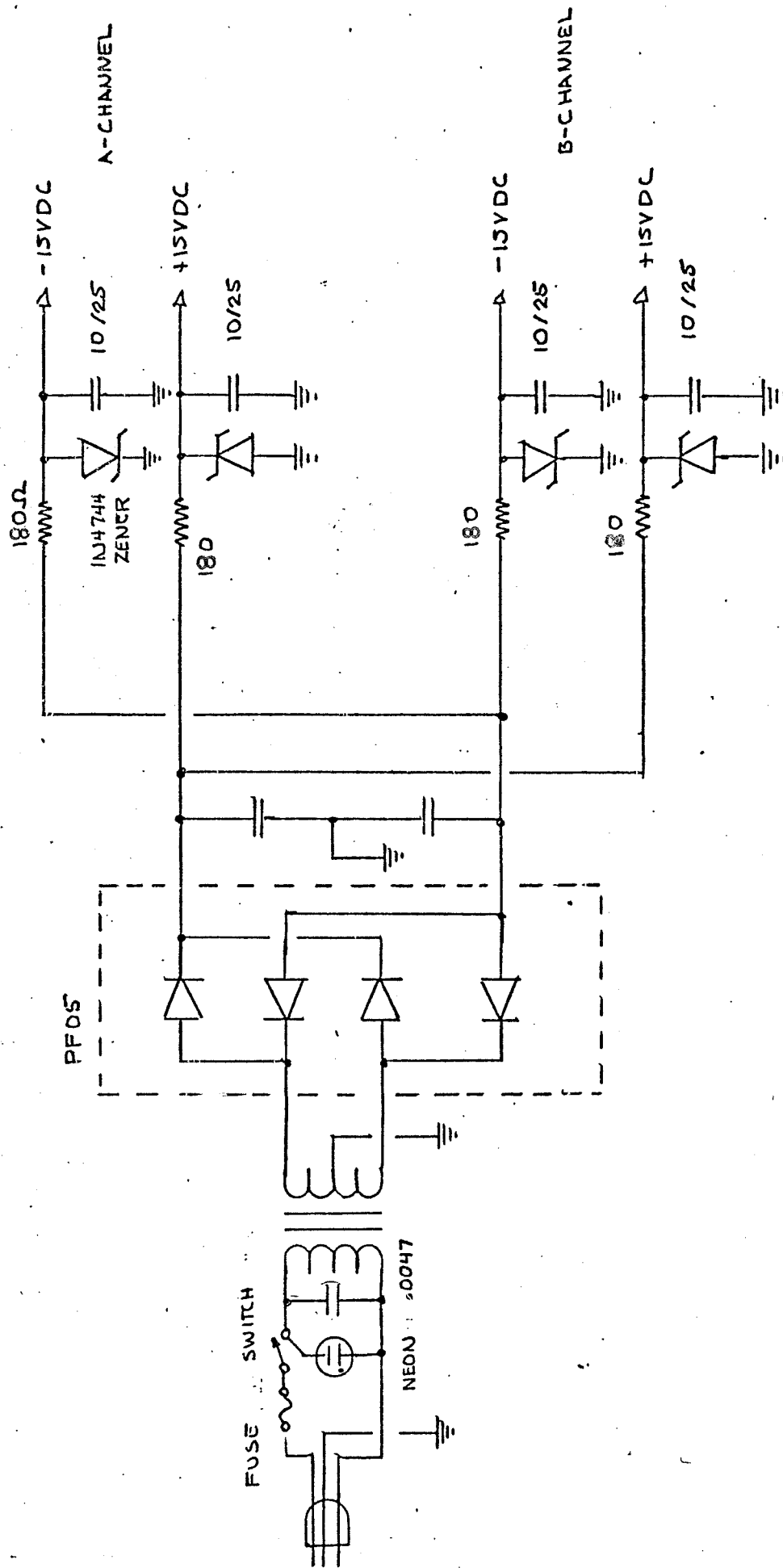
R10 POWER SUPPLY  
(EARLY)



# ED110 A EARLY VERSION 210



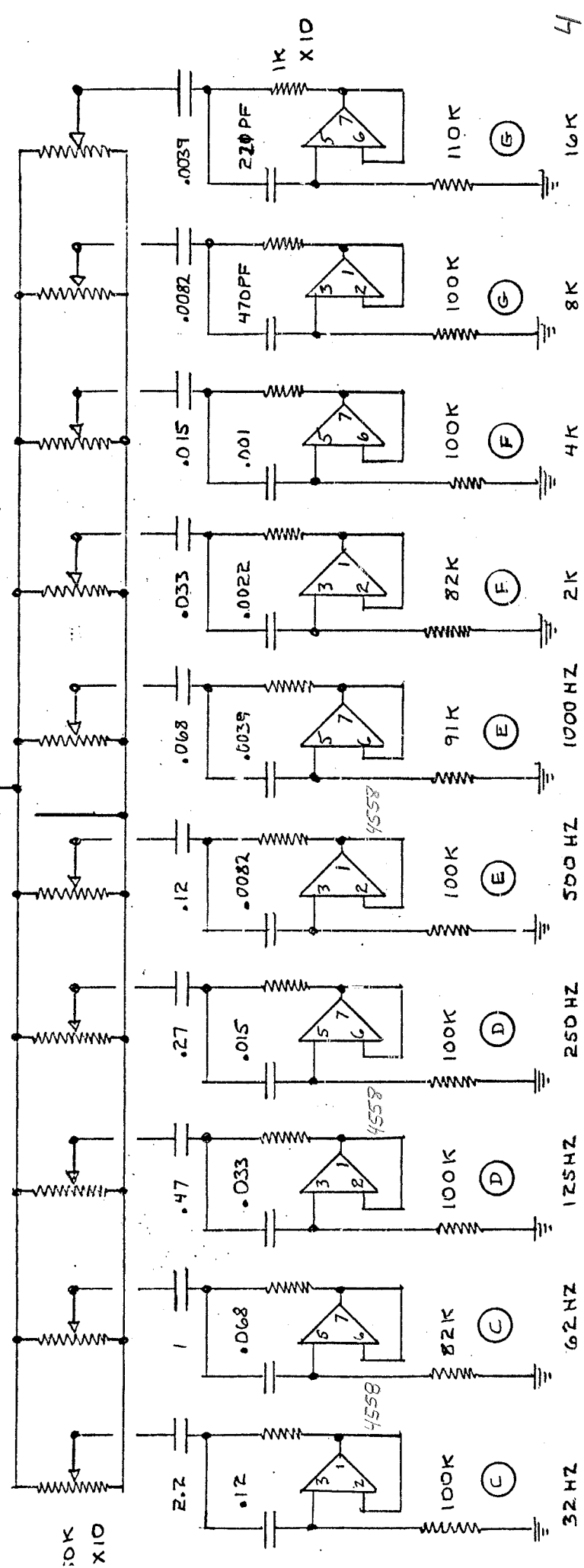
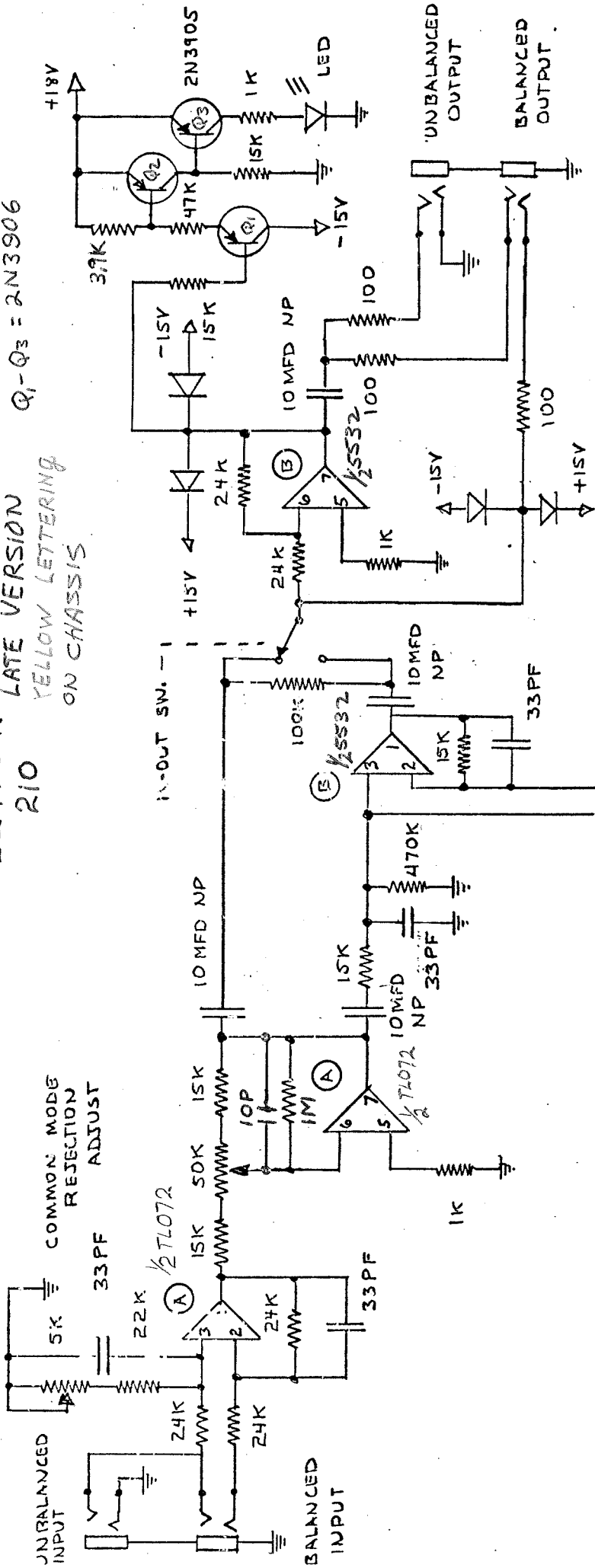
BIO POWER SUPPLY  
(LATE)

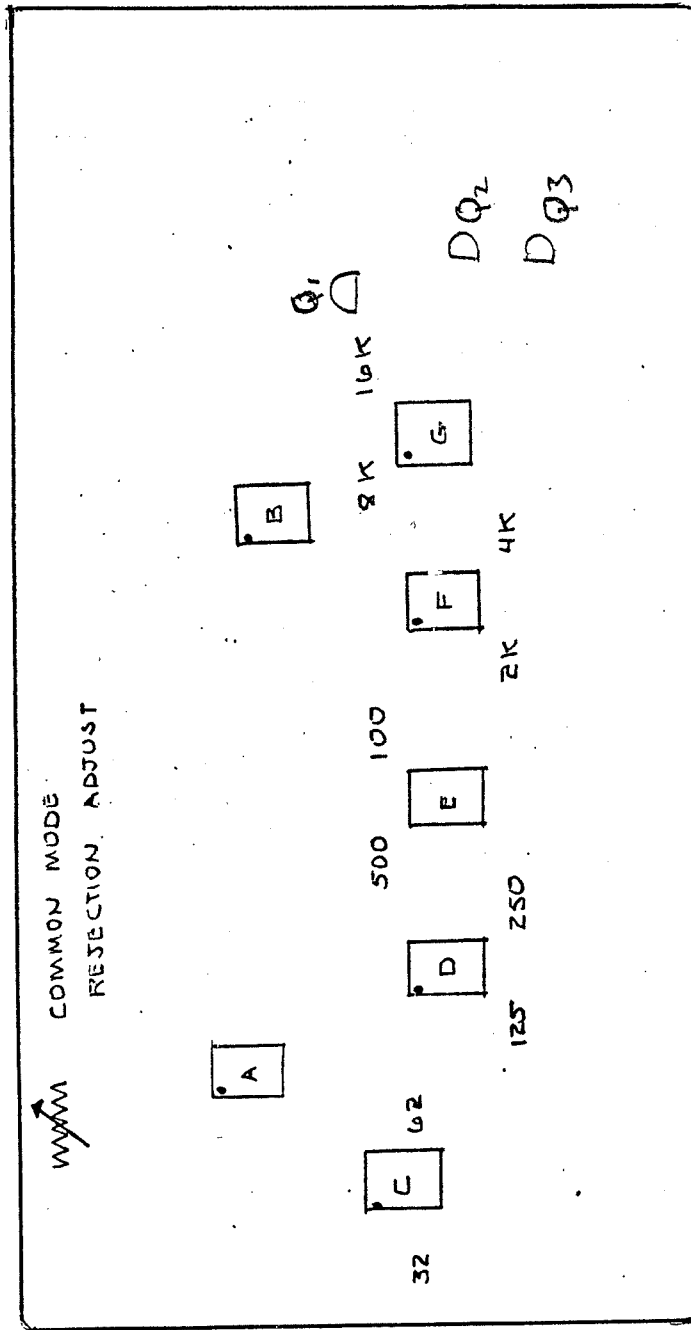


EQ 110 A  
R10

LATE VERSION  
YELLOW LETTERING  
ON CHASSIS

Q<sub>1</sub>-Q<sub>3</sub> = 2N3906





EQ 210 A  
I.C. LAYOUT  
COMPONENT SIDE  
OCT 18 1978

EQ 210 TEST PROCEDURE

1. VISUAL INSPECTION

FRONT PANEL

BACK PANEL

CKT BOARDS

SWITCHES

2. POWER SUPPLY

15VDC  $\pm$  0.5VDC (14.5-15.5)

3. NOISE

UNBALANCED OUTPUT METERING

-70dB MAXIMUM NOISE

4. SIGNAL CHECK

20HZ - 20KHZ SWEEP INPUT 0dB LEVEL UNBALANCED  
INPUT

A. SIGNAL OUTPUT UNBALANCED

SHOULD BE A -6dB LOSS OF FLAT SWEEP SIGNAL

B. MOVE SIGNAL INPUT TO BALANCED INPUT

SHOULD BE UNITY GAIN

C. MOVE EACH FREQ SLIDE POT

CHECK CUT AND BOOST

CHECK FOR INTERMITTENT POT OPERATION

D. CHECK MASTER GAIN CONTROL

E. REDUCE MASTER GAIN CONTROL TO MINIMUM

BOOST ALL FREQ CONTROLS TO MAXIMUM

CHECK FOR SEPARATE PEAKS OF EACH FREQ CONTROL

- F. RETURN ALL FREQ CONTROLS TO CENTER POSITION  
RAISE ONE FREQ CONTROL AND MASTER GAIN  
CONTROL TO MAXIMUM CHECK FOR CLIPPING  
WITH LED INDICATOR
- G. CHECK IN-OUT SWITCH OPERATION  
BOOST ONE CHANNEL MOVE SWITCH TO OUT  
AND CHECK FOR GAIN CHANGE.
5. COMMON MODE REJECTION CALIBRATION  
1KHZ SINE WAVE INPUT WITH TIP AND RING  
SHORTED TOGETHER  
TURN TRIM POT FOR MINIMUM SIGNAL OUTPUT  
OF UNIT  
(-70dB MAXIMUM SIGNAL)
6. BALANCED OUTPUT CHECK  
CHECK WITH PROBE ACROSS BOTH 100 OHM RESISTORS  
FOR EQUAL SIGNALS
7. AFFIX TESTED STICKER.