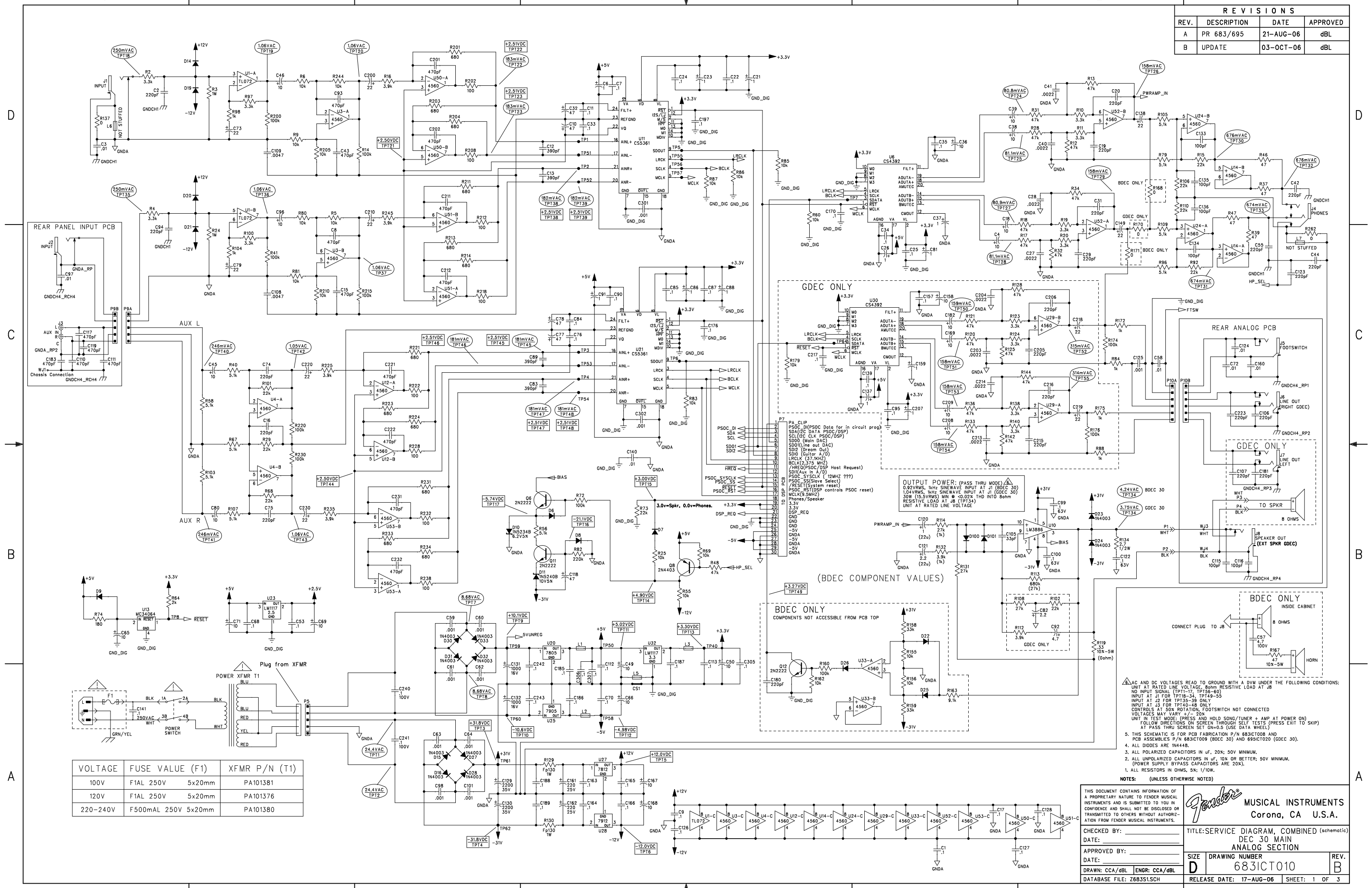


REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED
A	PR 683/695	21-AUG-06	dBL
B	UPDATE	03-OCT-06	dBL



VOLTAGE	FUSE VALUE (F1)	XFMR P/N (T1)
100V	F1AL 250V	5x20mm PA101381
120V	F1AL 250V	5x20mm PA101376
220-240V	F500mAL 250V	5x20mm PA101380

OUTPUT POWER: (PASS THRU MODE)
 0.92VRMS, I_{W1} SINEWAVE INPUT AT J1 (BDEC 30)
 1.04VRMS, I_{W2} SINEWAVE INPUT AT J1 (GDEC 30)
 30W (15.5VRMS) MIN @ $\leq 0.02\%$ THD INTO Bohm
 RESISTIVE LOAD AT J8 (TPT34)
 UNIT AT RATED LINE VOLTAGE

AC AND DC VOLTAGES READ TO GROUND WITH A DVM UNDER THE FOLLOWING CONDITIONS:
 NO INPUT SIGNAL (TPT1-17, TPT38-60)
 INPUT AT J1 FOR TPT18-34, TPT49-55
 INPUT AT J2 FOR TPT35-39 ONLY
 INPUT AT J3 FOR TPT40-48 ONLY
 CONTROLS AT 50% ROTATION, FOOTSWITCH NOT CONNECTED
 VOLTAGES MAY VARY +/- 20%
 UNIT IN TEST MODE: PRESS AND HOLD SONG/TUNER + AMP AT POWER ON)
 INPUT AT J3 THROUGH SCREEN SET (GND=0.5 (USE DATA WHEEL))
 FOLLOW DIRECTIONS ON SCREEN THROUGH SELF TESTS (PRESS EXIT TO SKIP)
 AT PASS THRU SCREEN SET GND=0.5 (USE DATA WHEEL)

- THIS SCHEMATIC IS FOR PCB FABRICATION P/N 683ICT008 AND PCB ASSEMBLY P/N 683ICT009 (BDEC 30) AND 693ICT020 (GDEC 30).
- ALL DIODES ARE 1N4448.
- ALL POLARIZED CAPACITORS IN uF, 20%; 50V MINIMUM.
- ALL UNPOLARIZED CAPACITORS IN uF, 10% OR BETTER; 50V MINIMUM. (POWER SUPPLY BYPASS CAPACITORS ARE 20%).
- ALL RESISTORS IN OHMS, 5%; 1/10W.

NOTES: (UNLESS OTHERWISE NOTED)

THIS DOCUMENT CONTAINS INFORMATION OF A PROPRIETARY NATURE TO FENDER MUSICAL INSTRUMENTS AND IS SUBMITTED TO YOU IN CONFERENCE AND SHALL NOT BE DISCLOSED OR TRANSMITTED TO OTHERS WITHOUT AUTHORIZATION FROM FENDER MUSICAL INSTRUMENTS.

Fender MUSICAL INSTRUMENTS
 Corona, CA U.S.A.

CHECKED BY: _____
 DATE: _____

APPROVED BY: _____
 DATE: _____

DRAWN: CCA/dBL ENGR: CCA/dBL
 DATABASE FILE: 268351.SCH

TITLE: SERVICE DIAGRAM, COMBINED (schematic)
 DEC 30 MAIN ANALOG SECTION

SIZE: **D** DRAWING NUMBER: 683ICT010 REV. **B**

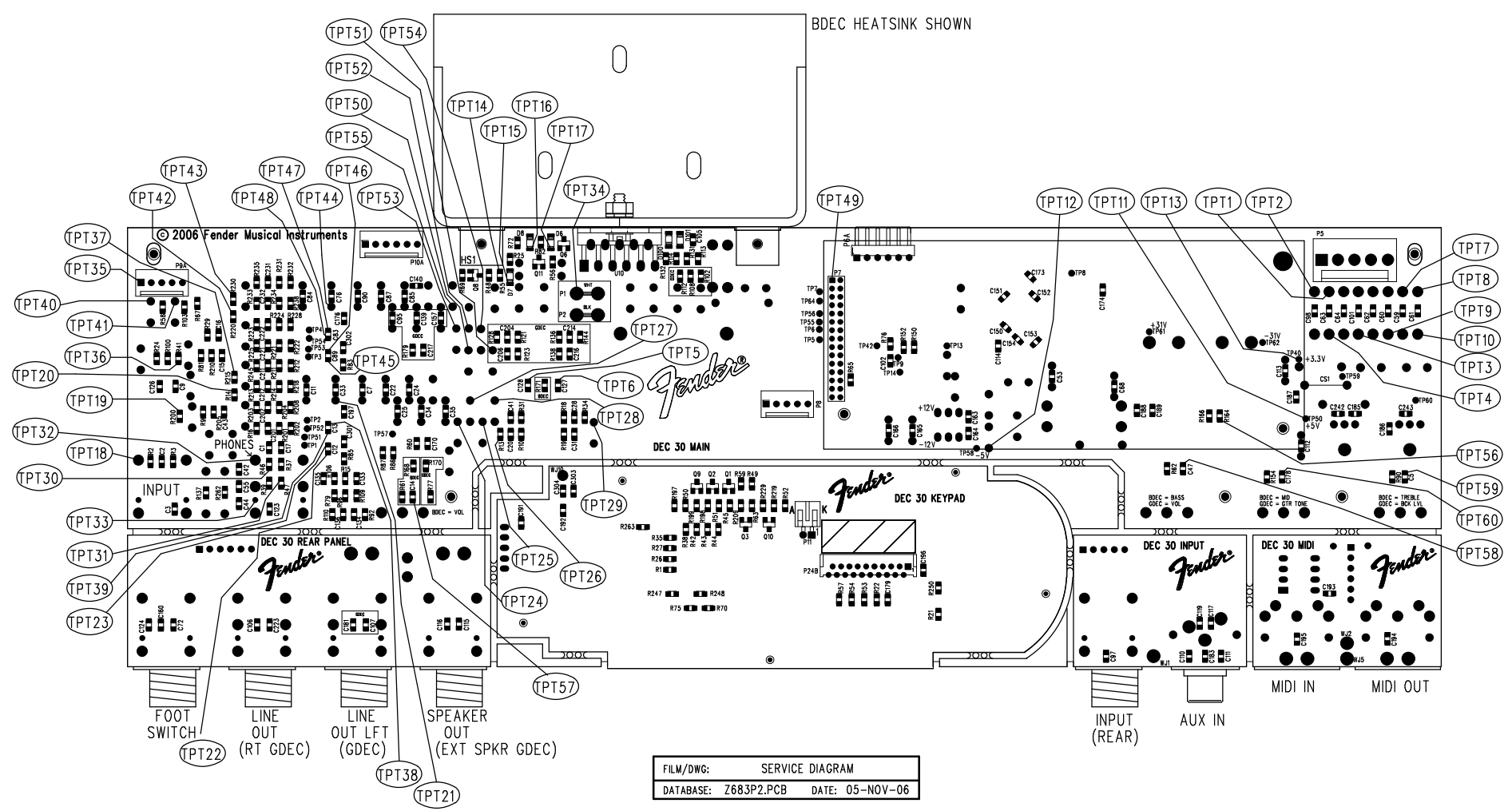
RELEASE DATE: 17-AUG-06 SHEET: 1 OF 3

8 7 6 5 4 3 2 1

REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED
A	PR 683/695	18-AUG-06	dBL
B	UPDATE	05-NOV-06	dBL

D
C
B
A

D
C
B
A



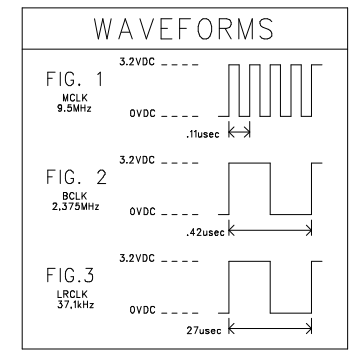
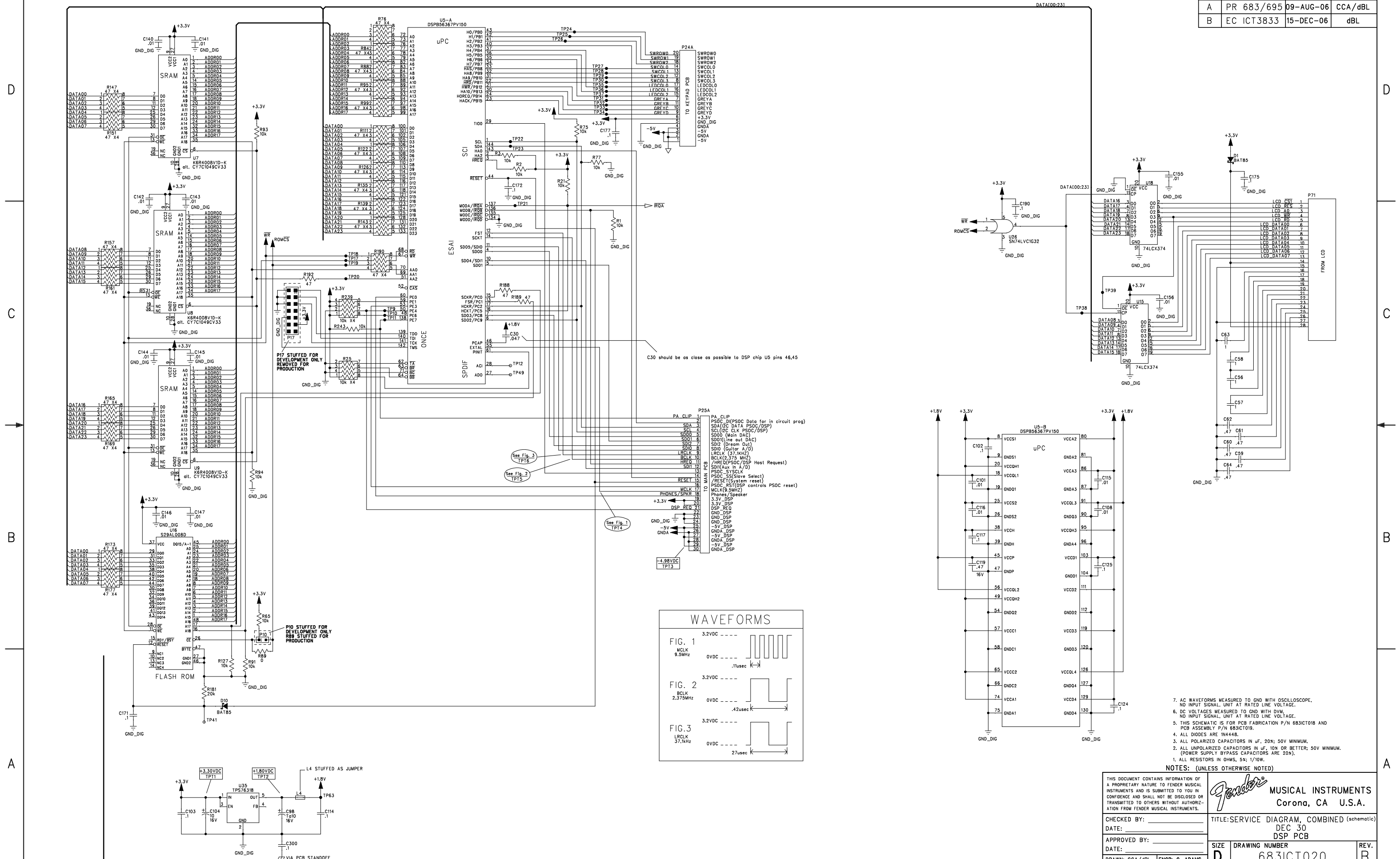
FILM/DWG: SERVICE DIAGRAM
 DATABASE: Z683P2.PCB DATE: 05-NOV-06

1. SEE SHEETS 1-2 FOR PRIMARY WIRING, TEST CONDITIONS, TEST POINT VALUES, AND STUFFING DIFFERENCES BETWEEN MODELS.
 NOTES: (UNLESS OTHERWISE NOTED)

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CHECKED BY: _____ DATE: _____		TITLE: SERVICE DIAGRAM, COMBINED (PCB assy) DEC 30 MAIN PCB	
APPROVED BY: _____ DATE: _____		SIZE C	DRAWING NUMBER 683ICT010
DRAWN: D. Lewis	ENGR: D. Lewis	RELEASE DATE: 17-AUG-06	REV. B SHEET 3 OF 3
DATABASE FILE: Z683P1.PCB		SHEET 3 OF 3	

8 7 6 5 4 3 2 1

REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED
A	PR 683/695	09-AUG-06	CCA/dBL
B	EC ICT3833	15-DEC-06	dBL



7. AC WAVEFORMS MEASURED TO GND WITH OSCILLOSCOPE. NO INPUT SIGNAL, UNIT AT RATED LINE VOLTAGE.
8. DC VOLTAGES MEASURED TO GND WITH DVM. NO INPUT SIGNAL, UNIT AT RATED LINE VOLTAGE.
9. THIS SCHEMATIC IS FOR PCB FABRICATION P/N 683ICT018 AND PCB ASSEMBLY P/N 683ICT019.
10. ALL DIODES ARE IN4448.
11. ALL POLARIZED CAPACITORS IN μ F, 20%; 50V MINIMUM.
12. ALL UNPOLARIZED CAPACITORS IN μ F, 10% OR BETTER; 50V MINIMUM. (POWER SUPPLY BYPASS CAPACITORS ARE 20%).
13. ALL RESISTORS IN OHMS, 5%; 1/10W.

THIS DOCUMENT CONTAINS INFORMATION OF A PROPRIETARY NATURE TO FENDER MUSICAL INSTRUMENTS AND IS SUBMITTED TO YOU IN CONFERENCE AND SHALL NOT BE DISCLOSED OR TRANSMITTED TO OTHERS WITHOUT AUTHORIZATION FROM FENDER MUSICAL INSTRUMENTS.

Fender MUSICAL INSTRUMENTS
Corona, CA U.S.A.

CHECKED BY: _____
DATE: _____

APPROVED BY: _____
DATE: _____

DRAWN: CCA/dBL ENGR: C. ADAMS
DATABASE FILE: DEC30_DSP.SCH

TITLE: SERVICE DIAGRAM, COMBINED (schematic)
DEC 30
DSP PCB

SIZE: **D**
DRAWING NUMBER: 683ICT020
REV. **B**

RELEASE DATE: 09-AUG-06
SHEET: 1 OF 2

A

A

B

B

C

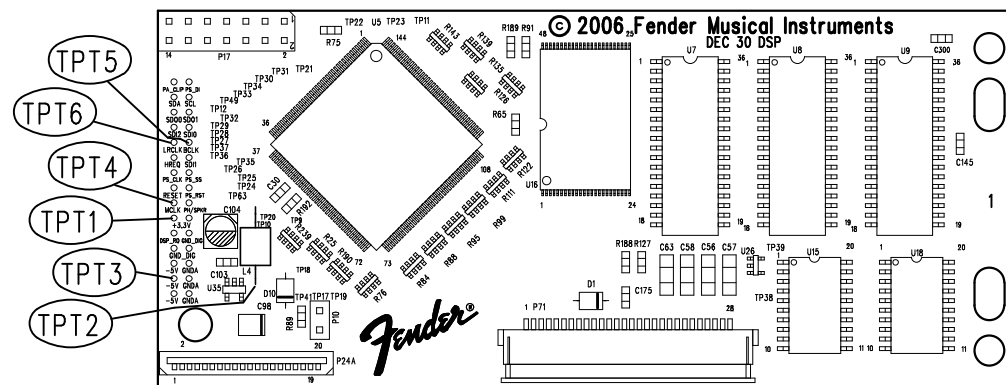
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D

D


REVISIONS

REV.	DESCRIPTION	DATE	APPROVED
A	PR683/695	16-AUG-06	dBL
B	EC ICT3833	15-DEC-06	dBL



FILM/DWG: SERVICE DIAGRAM
 DATABASE: Z683P2.PCB DATE: 15-DEC-06

- 2. L4 STUFFED WITH JUMPER.
 - 1. SEE SHEET 1 FOR TEST POINT VALUES.
- NOTES: (UNLESS OTHERWISE NOTED)

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CHECKED BY: _____ DATE: _____		TITLE: SERVICE DIAGRAM, COMBINED (PCB assy) DEC 30 DSP PCB	
APPROVED BY: _____ DATE: _____		SIZE B	DRAWING NUMBER 683ICT020
DRAWN: CCA/dBL	ENGR: CCA/dBL	REV. B	
DATABASE FILE: Z683P2.PCB		RELEASE DATE: 05-NOV-06	SHEET 2 OF 2