



6. VOLTAGE TEST POINTS ARE: xxV , SLO-BLO
- ⑤ SELECTED VALUES TO MATCH T4A OR B FOR PROPER 'DB GAIN REDUCTION' READING.
- ④ FOR 220 V CONNECTION, SEE MANUAL.
3. PATENT N° 3258707 ON PORTIONS OF THIS EQUIPMENT.
2. CAPACITORS IN μFD .
1. RESISTORS IN OHMS, $\frac{1}{2}$ W, 5%.

NOTES, UNLESS SPECIFIED:

C: SCHEMATIC CORRECTIONS, NO CIRCUIT CHANGES.
 A: 3-30-72
 B: 1-10-73
 REV'D, RESPONSE-DIV UP
 BY S-10-11-73 JMB

TELETRONIX LA-2A	
UNITED RECORDING ELECTRONICS INDUSTRIES	SCHEMATIC, LEVELING AMPLIFIER C-10953 C
REP JJA	DATE 8 AUG 68
DEPARTMENT LOS ANGELES CALIFORNIA	CHECKED ISSUE 3 APPROVED