

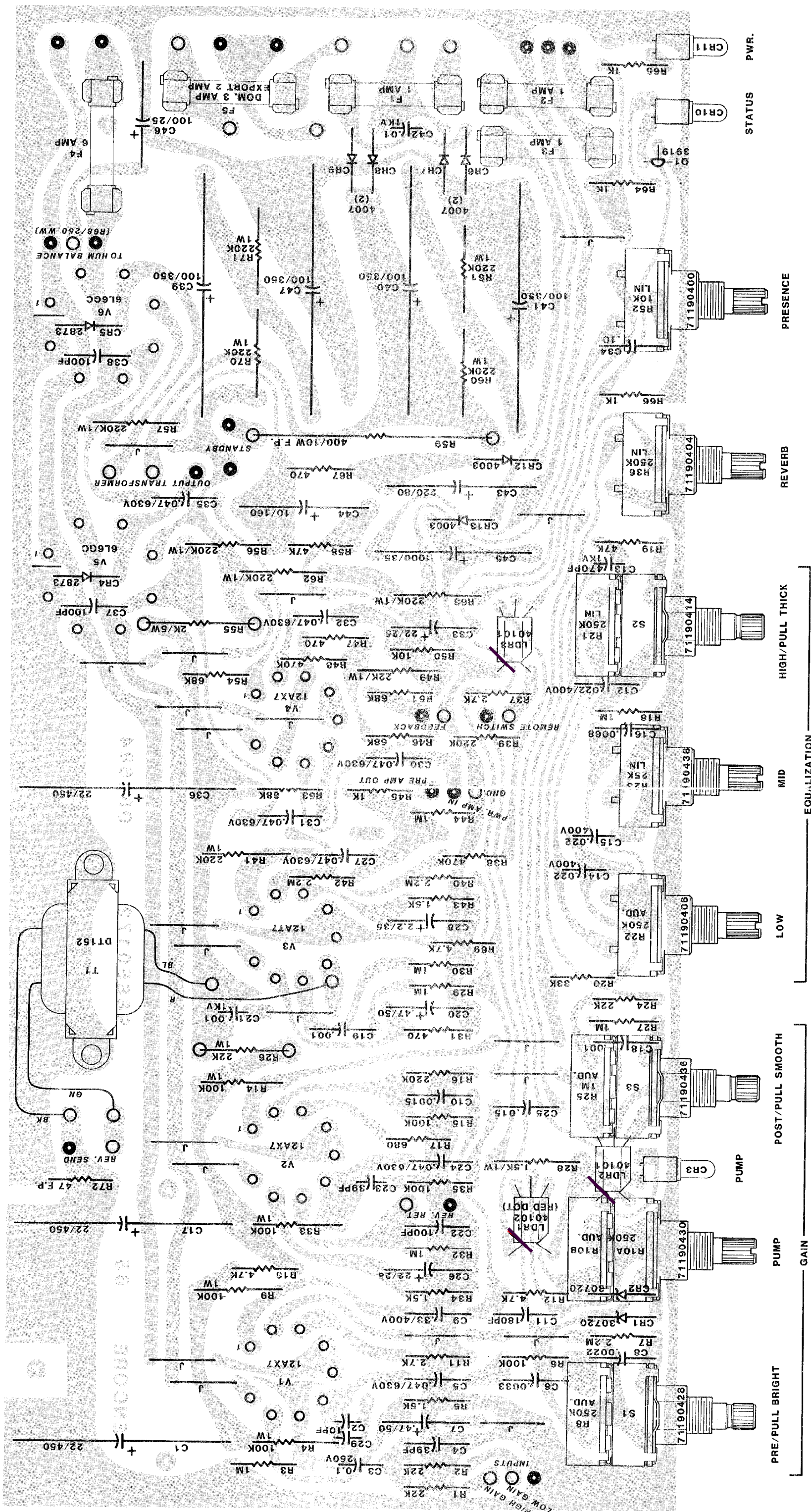
NOTES
 ALL POTS ARE LINEAR UNLESS NOTED
 ALL RESISTORS 1/4W UNLESS NOTED
 ALL RESISTORS IN OHMS UNLESS NOTED
 ALL CAPACITORS IN MFD UNLESS NOTED
 ALL DIODES IN 4148 UNLESS NOTED
 LAST REF. DESIGNATOR USED:
 R72 C49 CR13 S6 J6 Q1 F5 LDR3

ENCORE 65
 BD. # 98850171 (10/3/83)
 BD. # 98850172 (6/16/84)

REVISED 9/12/84 *REK*
 PEAVEY ELECTRONICS CORP.
 MERIDIAN, M.S.
 DRAWN BY *REK* DATE 10/26/83
 C.B. CK. *REK*
 CK. BY *REK*
 APPR. BY *REK*

P/N 81501141

COPYRIGHT 1983 PRINTED IN U.S.A.



*Please Note: The LDR's are drawn incorrectly on the layout. The flats should be positioned away from the pots as per the added lines. The schematic itself is correct.

ENCORE 65