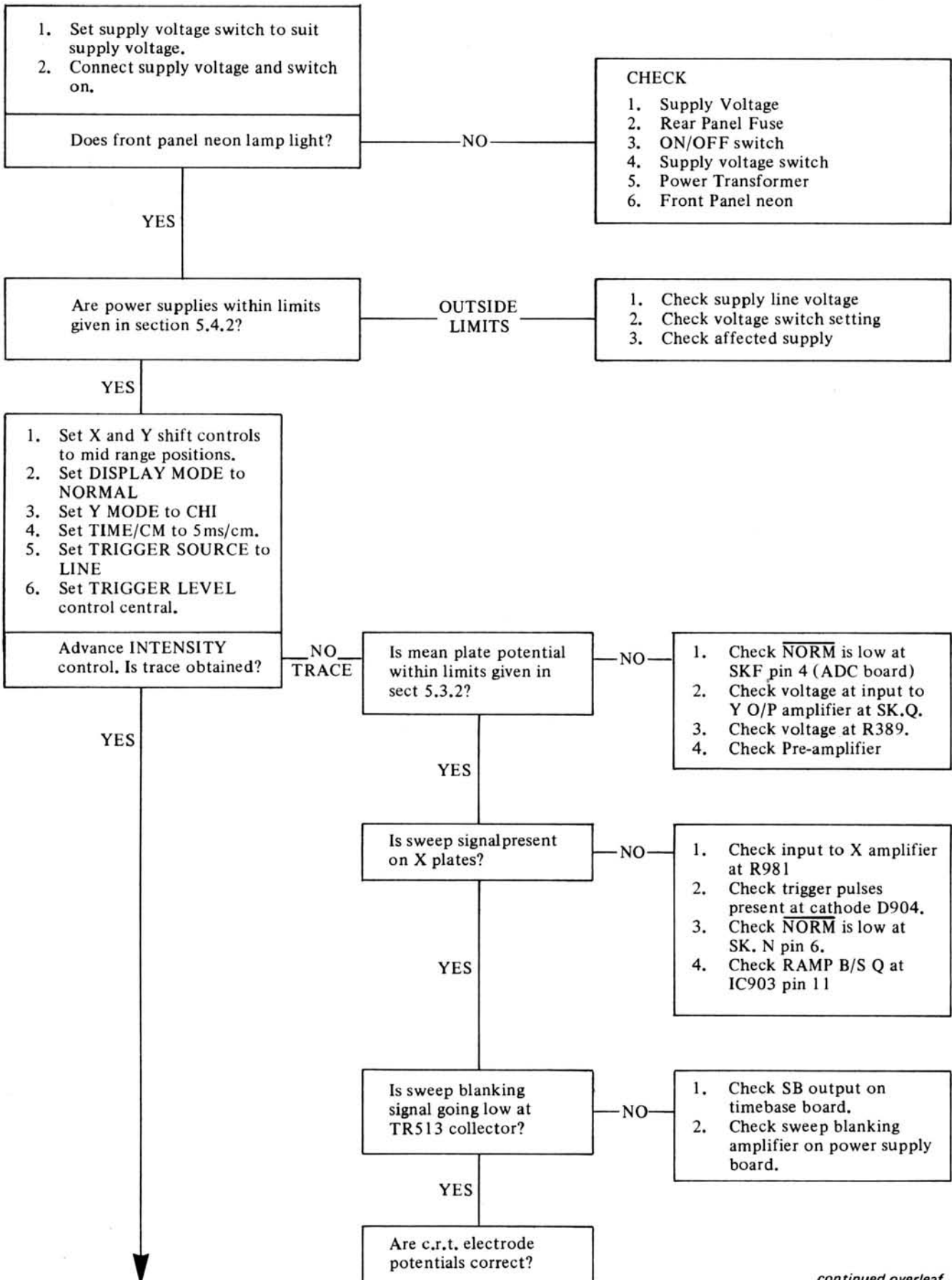


	Display Ramp Start	Display Ramp Stop	Read Address Clock	Write Address Clock	L.S.Bit Write Address	L.S.Bit Read Address	Dot Joiner Sample & Hold (S & H) drive	Dot Joiner Time constant (TC) controls				50% Hold conditions	Special Functions	Y Channel Beam Switch
								A	B	C	D			
Normal Mode Single Trace	From Refresh B/S Q	Analogue via TR919	0,909 MHz	—	—	—	—	—	—	—	—	Refresh Mode Selected on R1 19/23	—	
Normal Mode Dual Trace	"	"	"	—	—	—	—	—	—	—	—	—	Ramp B/S Q clocks IC 708 b, on R1/11 (ALT) 2nd stage R.A.C. drives IC 708 b, clear to give chop (R12/18)	
Refreshed Mode Single Trace (Released)	From R.A.C. M.S.B. via Delay Circuit	From R.A.C. M.S.B. via Delay Circuit	0,909 MHz from IC 728a, b, via gate IC 707	From Range Divider - See Table for frequency	From Write Address Counter (W.A.C.)	From Read Address Counter (R.A.C.)	P4 Pulse at 0,909 MHz rate via gate IC 732a	0	1	0	1	50% Hold drives L.S.B. Write Address high and sets up Dual Trace conditions for Read Address and Dot joiner (Condition 1)	—	
Refreshed Mode Ranges 7/23 Dual Trace (Released)	"	"	"	"	"	From B/S IC 723a via gate IC 719	P4 Pulse at 0,455 MHz rate via IC 725a, b, & IC 732a	0	0	1	1	50% Hold drives L.S.B. write address high. (Condition 2)	From L.S.B. W.A.C.	
Refreshed Mode Range 1/6 Single Trace (Released)	"	Analogue via TR 919	1,818 MHz from PI via IC 720b	1,818 MHz on all 6 ranges	"	From Read Address Counter (R.A.C.)	P4 Pulse at 1,818 MHz rate via IC 725a, b, & IC 732a	1	1	0	0	Condition 1	Write sequence stops read, Then two read sweeps before trigger is enabled	—
Refreshed Mode Ranges 1/6 Dual Trace (Released)	"	"	"	"	"	From B/S IC 723a via gate IC 719	P4 Pulse at 0,909 MHz rate via gate IC 732a	1	0	1	0	Condition 2	"	From L.S.B. W.A.C.
Roll Mode Ranges 7/23 Single Trace (Released)	From Coincidence gate via delay	From Coincidence gate via delay	0,909 MHz from IC 728a, b, via gate IC 707	From Range Divider - See Table for frequency	From Write Address Counter	From Read Address Counter	P4 Pulse at 0,909 MHz rate via gate IC 732a	0	1	0	1	Condition 1	—	—
Roll Mode Range 7/23 Dual Trace (Released)	"	"	"	"	"	From B/S IC 723a via gate IC 719	P4 Pulse at 0,455 MHz rate via IC 725a, b, & IC 732a	0	0	1	1	Condition 2	—	From L.S.B. W.A.C.
Roll Mode Range 1/6 Single Trace (Released)	From M.S.B. R.A.C.	From M.S.B. R.A.C.	1,818 MHz from PI via IC 720b	1,818 MHz on all 6 ranges	"	From RAC	1,818 MHz rate via gate IC 732a	1	1	0	0	Condition 1	Displayed information is set by W.A.C. until "STORE" Condition is reached when R.A.C. addresses store.	—
Roll Mode Range 1/6 Dual Trace (Released)	"	"	"	"	"	From B/S IC 723a via gate IC 719	0,909 MHz rate via gate IC 732a	1	0	1	0	Condition 2	"	From L.S.B. W.A.C.

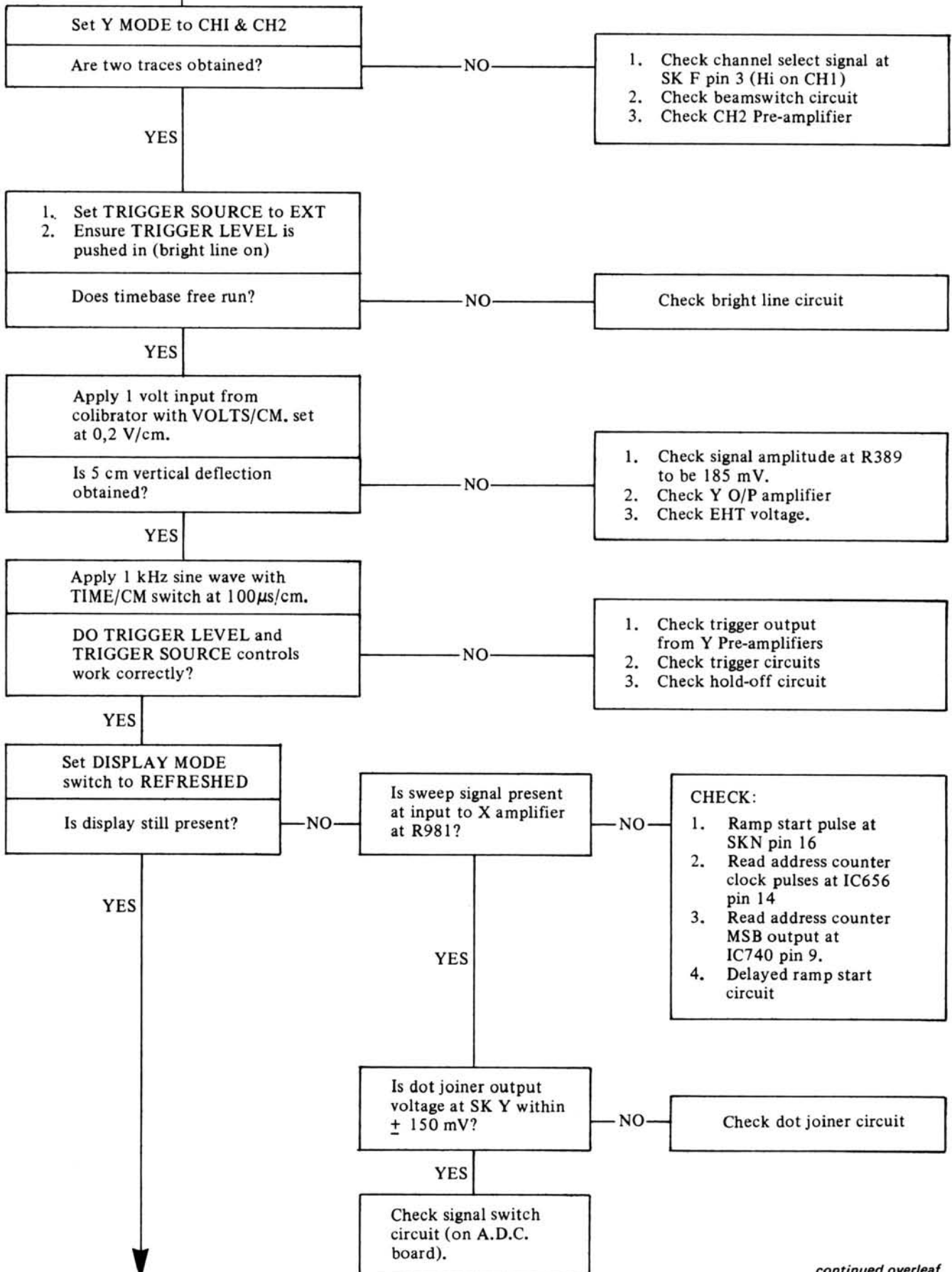
Fig.17 Control Condition Table

SECTION 5.3.1 FAULT LOCALISATION PROCEDURE



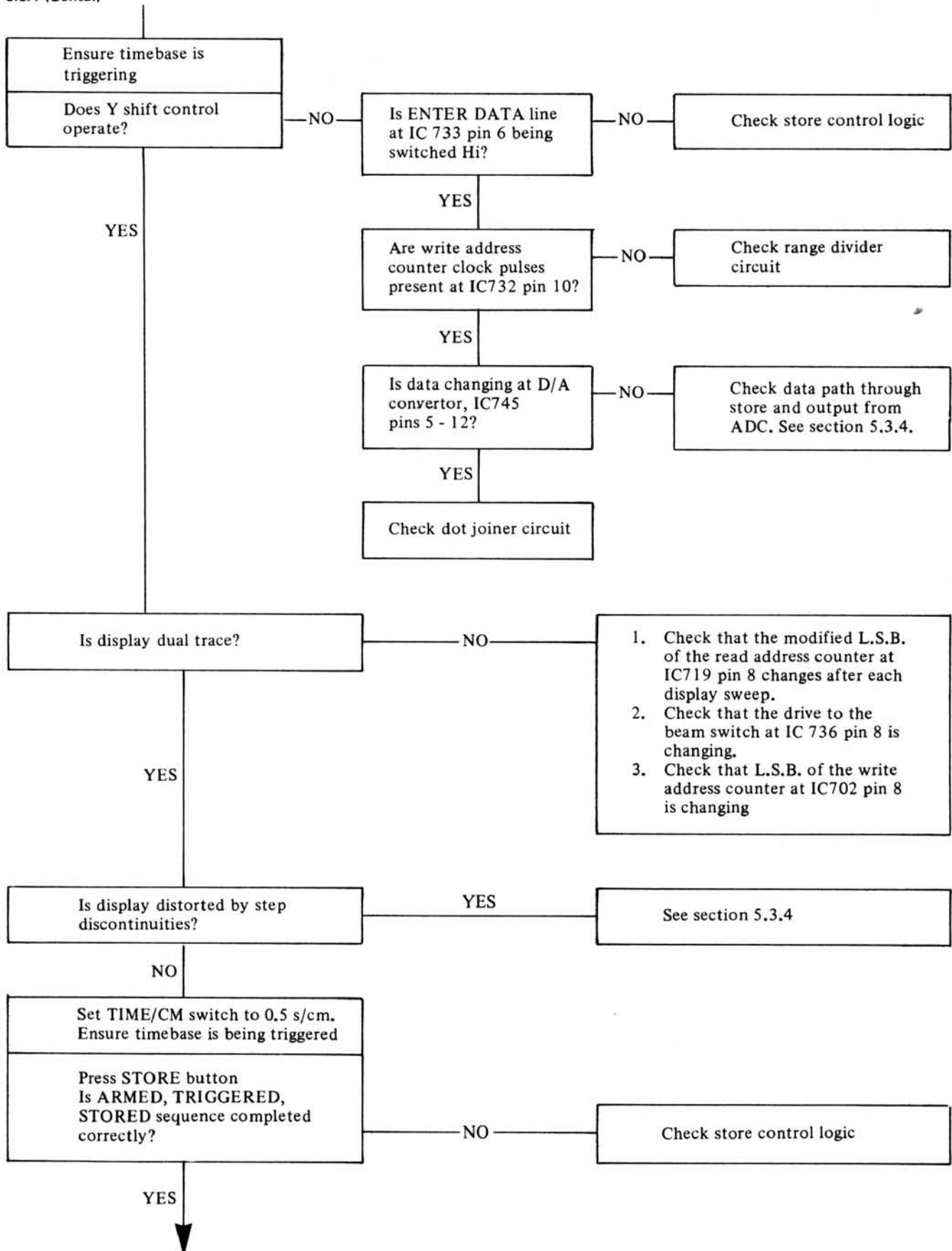
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5.3.1 (Contd.)



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5.3.1 (Contd.)



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5.3.1 (Contd.)

